|  |  |  |  |  |  |
| --- | --- | --- | --- | --- | --- |
| Decimal | a | b | c | d |  |
| 0 | 0 | 0 | 0 | 0 | 0 |
| 1 | 0 | 0 | 0 | 1 | 0 |
| 2 | 0 | 0 | 1 | 0 | 1 |
| 3 | 0 | 0 | 1 | 1 | 1 |
| 4 | 0 | 1 | 0 | 0 | 0 |
| 5 | 0 | 1 | 0 | 1 | 1 |
| 6 | 0 | 1 | 1 | 0 | 0 |
| 7 | 0 | 1 | 1 | 1 | 1 |
| 8 | 1 | 0 | 0 | 0 | 0 |
| 9 | 1 | 0 | 0 | 1 | 0 |
| 10 | 1 | 0 | 1 | 0 | 1 |
| 11 | 1 | 0 | 1 | 1 | 1 |
| 12 | 1 | 1 | 0 | 0 | 0 |
| 13 | 1 | 1 | 0 | 1 | 0 |
| 14 | 1 | 1 | 1 | 0 | 0 |
| 15 | 1 | 1 | 1 | 1 | 1 |

**Tarea 2 Naoufal el rhazzali/TEC. ELEC. II**

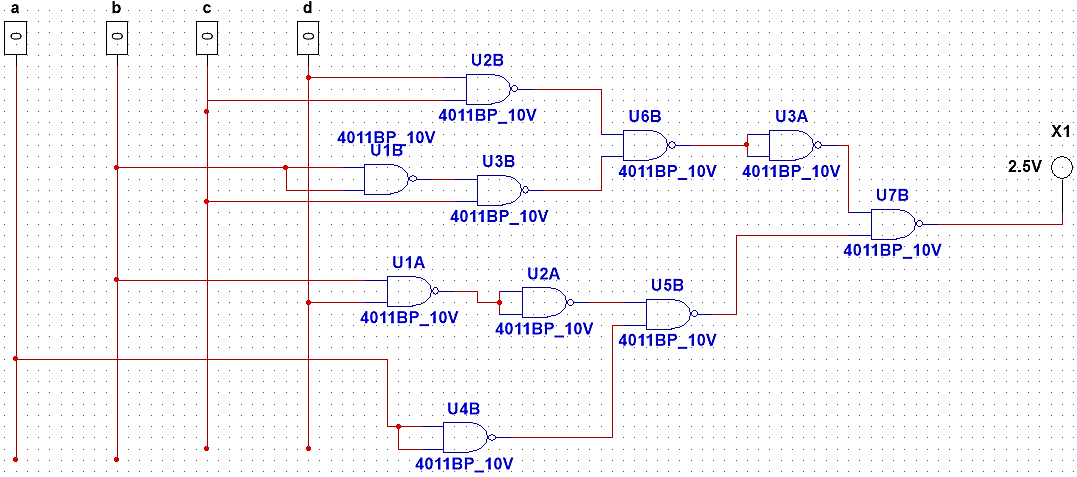
|  |  |  |  |  |
| --- | --- | --- | --- | --- |
| cd  ab | 00 | 01 | 11 | 10 |
| 00 | 0 | 0 | **1** | **1** |
| 01 | 0 | **1** | **1** | 0 |
| 11 | 0 | 0 | **1** | 0 |
| 10 | 0 | 0 | **1** | **1** |

1. Simplificar

→

()

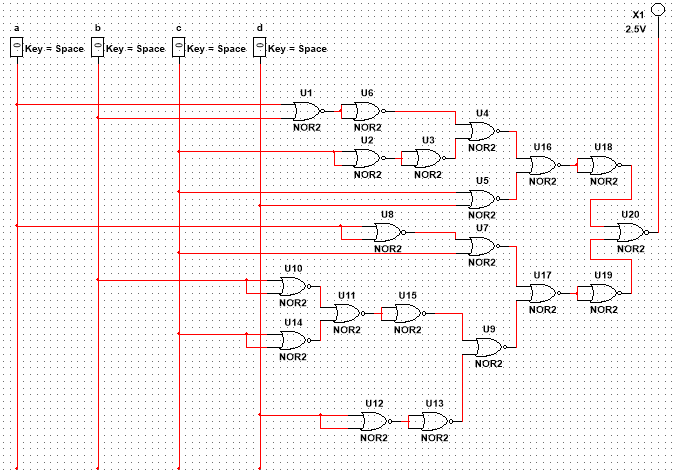
**→**

1. Implementar con Puertas NAND de dos entradas

*Implementación Con Puertas NAND*

1. Obtener la función del sistema expresada en términos máximos, simplificar e implementar con puertas NOR de dos entradas

|  |  |  |  |  |
| --- | --- | --- | --- | --- |
| cd  ab | 00 | 01 | 11 | 10 |
| 00 | **0** | **0** | 1 | 1 |
| 01 | **0** | 1 | 1 | **0** |
| 11 | **0** | **0** | 1 | **0** |
| 10 | **0** | **0** | 1 | 1 |

****

*Implementación Con Puertas NOR*